DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Organizes Workshop on
IMPLEMENTATION OF COMMUNICATION PROTOCOLS IN FPGA

RESOURCE PERSON

Mr.Kumaran.R
Senior Design Engineer, SmartSOC solutions, Chennai.

VENUE

VLSI LAB,ECE Department ,BSACIST

22.04.2022 &23.04.2022

09.30am to 03.00pm

Registration Fee:Rs.500/-(Gpay:9655537103)

Registration link:https://forms.gle/edBi1ny2fX3iWNi58

CO-ORDINATORS

Dr. V. Jean Shilpa,
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CONVENERS

Dr.D. NAJUMNISSA JAMAL,DEAN&HEAD(SECS)
DR.C.THARINI,PROFESSOR & HEAD/ECE

Last date for registration: 20.04.2022

Limited seats available*

Target Audience: All UG & PG students,Research Scholars and Faculty Members